IN THE CLAIMS:

Please add new claims 20-28 as follows:

--20.   (New) A microcomputer which performs information processing, comprising:

a processor that executes instructions;

an external bus that is connected to both an emulation memory and an external memory other than said emulation memory;

an internal memory that stores instructions; and

a bus controller that connects a bus of said processor to said external bus and switches an access of said processor to said internal memory to an access to said emulation memory through said external bus, when said microcomputer is in an emulation mode.--

--21.   (New) The microcomputer according to claim 20, further comprising:

a mode selection terminal that selects ON or OFF of the emulation mode.--

--22.   (New) The microcomputer according to claim 20, further comprising:

a mode selection register that is accessible by said processor and stores information used to select ON or OFF of the emulation mode.--

--23.   (New) The microcomputer according to claim 20,

wherein an address bus of said processor is connected to an external address bus and an address bus of said internal memory without dependent on ON/OFF of the emulation mode, and

wherein a data bus of said processor is connected to an external data bus when the emulation mode becomes ON.--

--24.   (New) The microcomputer according to claim 20, further comprising:

a memory controller that is connectable to said emulation memory and said external memory, and outputs a first control signal for controlling said external memory and a